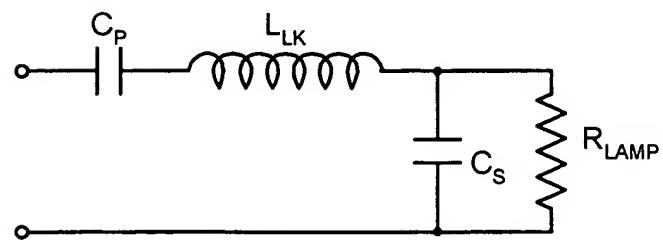
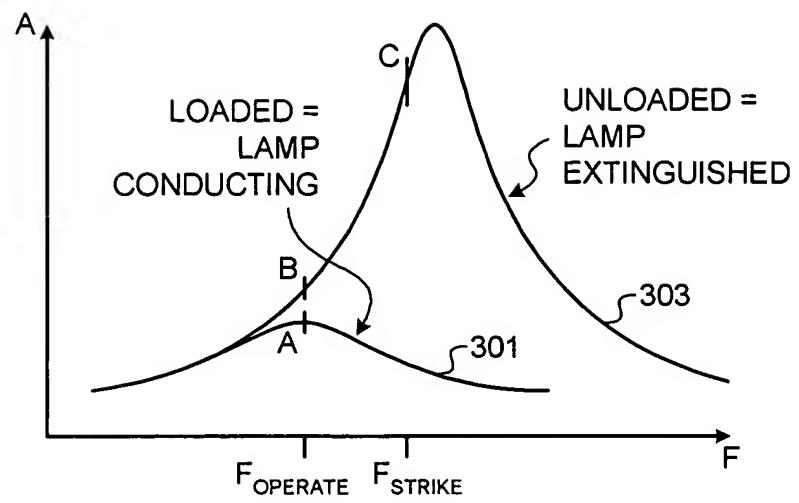
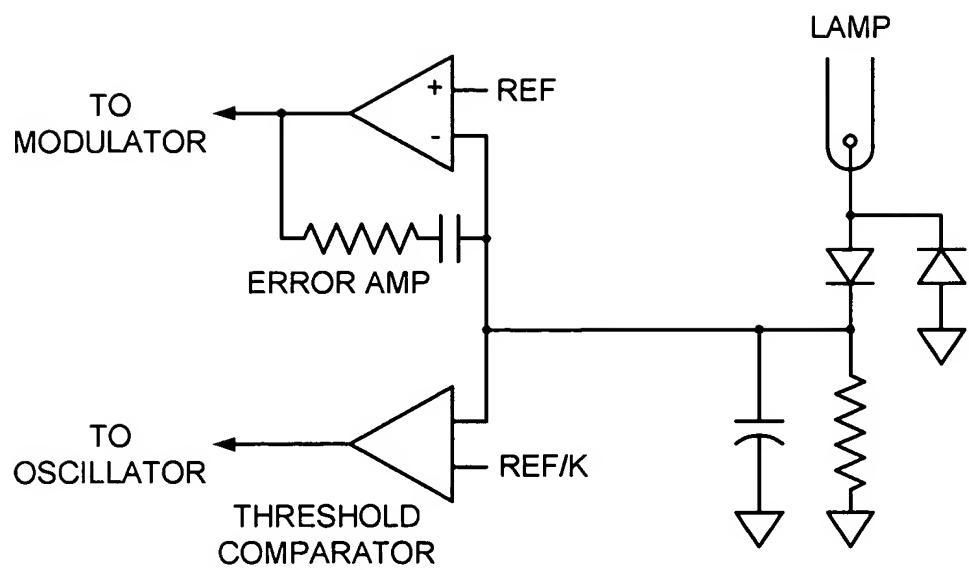


**FIGURE 1**  
(PRIOR ART)

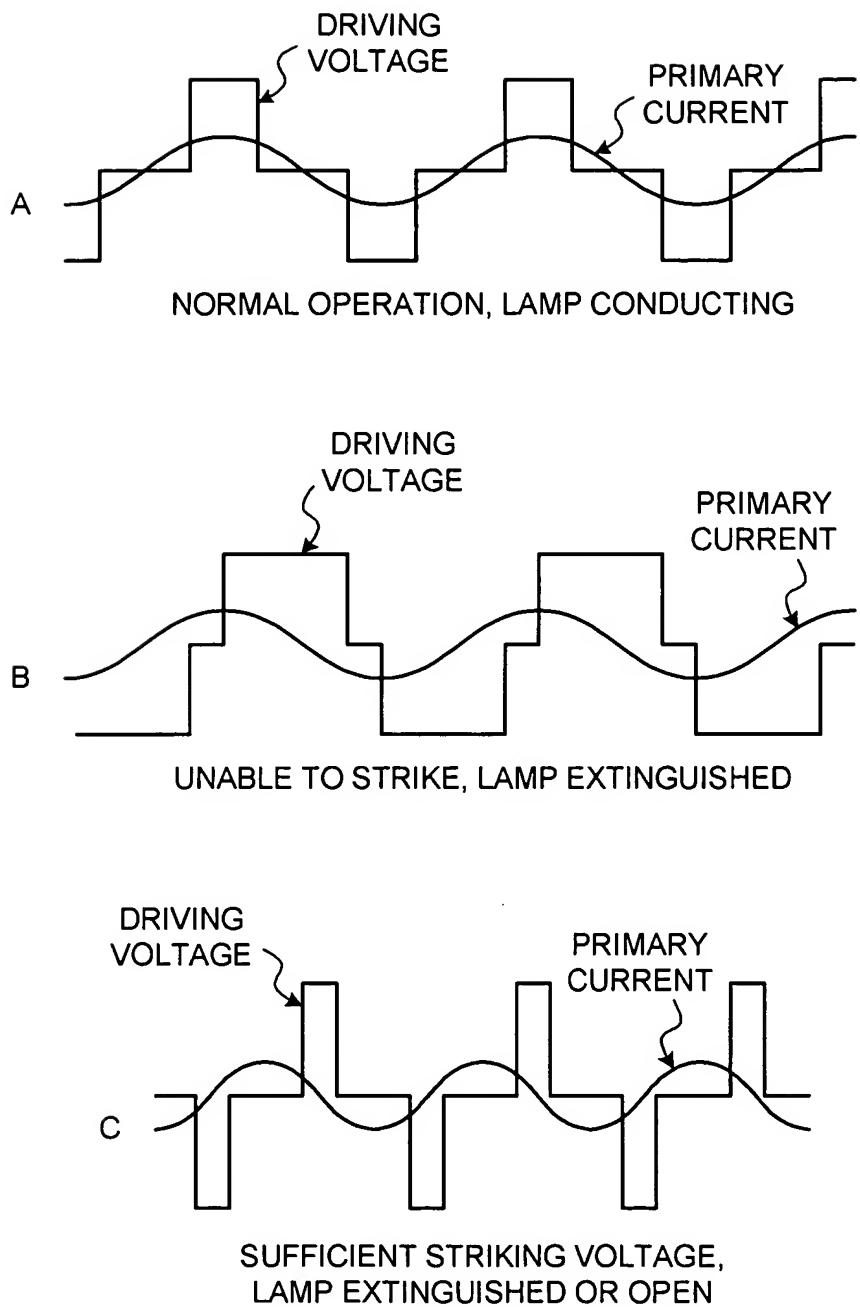


**FIGURE 2**  
(PRIOR ART)

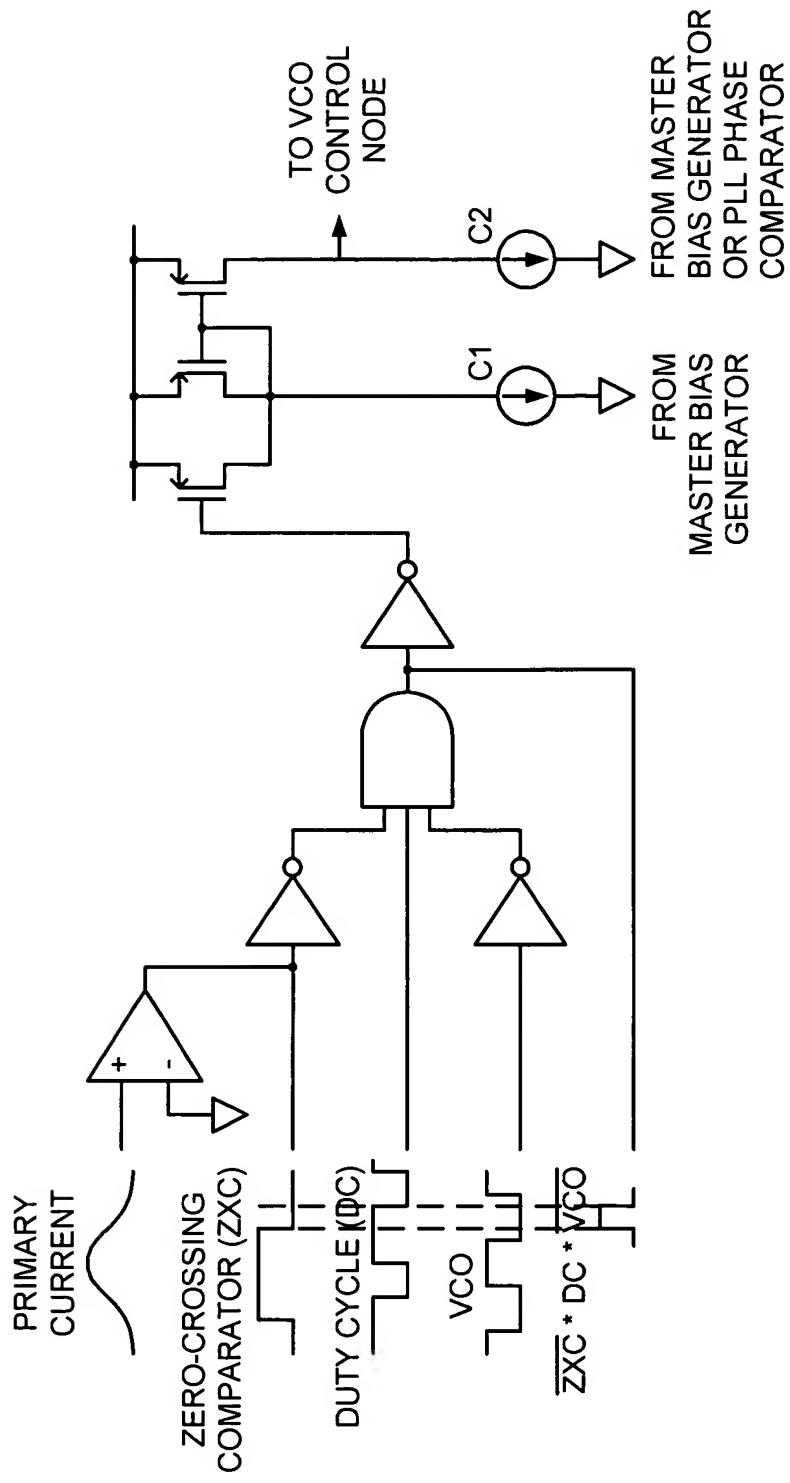
**FIGURE 3**



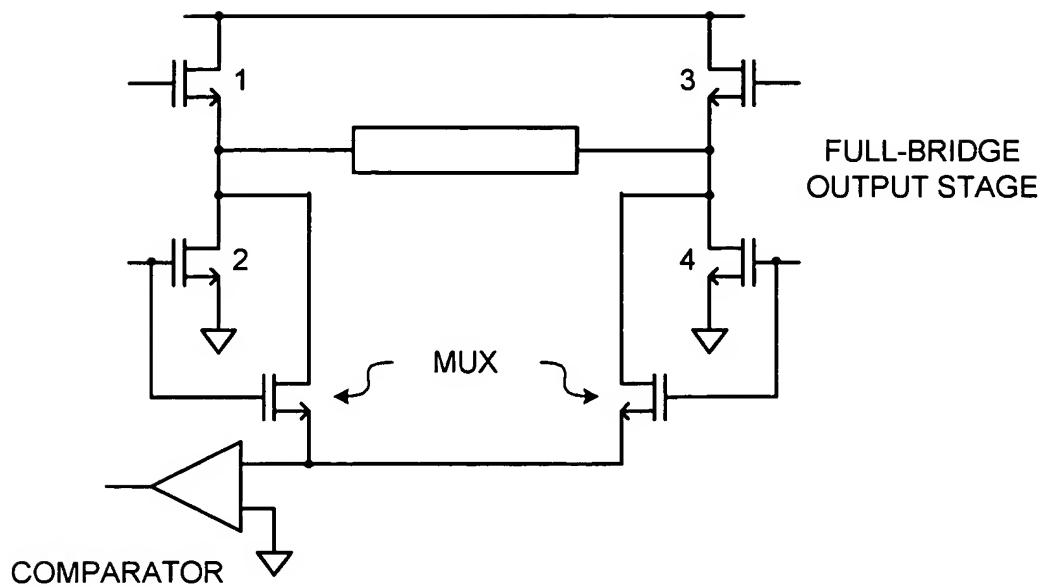
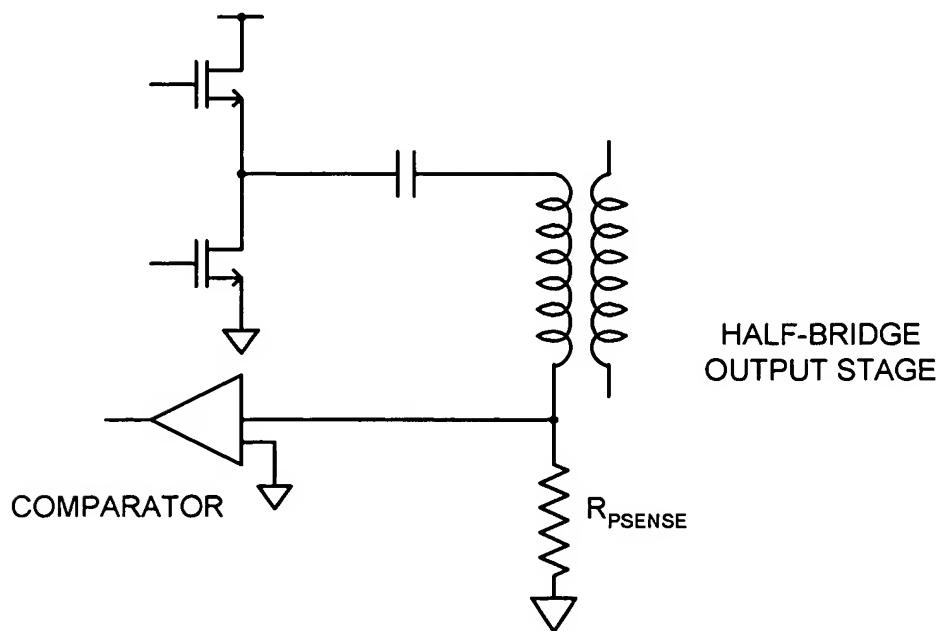
**FIGURE 4**  
(PRIOR ART)

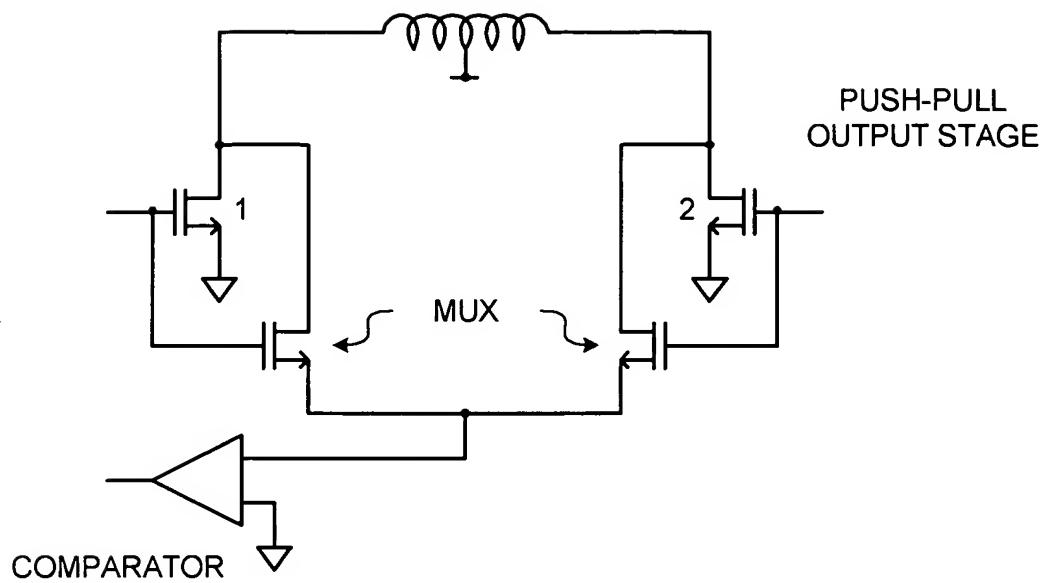


**FIGURE 5**  
**THE IDEA**



**FIGURE 6**  
VCO CONTROL LOGIC

**FIGURE 7****FIGURE 8**



**FIGURE 9**

